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ABSTRACT OF THE DISCLOSURE

Stepping drive control of a motor is effected by synchronizing interruption to a CPU with N (in number) line triggers set to N times of one-line of an image. Further, in a synchronous mode in which phase control is effected in synchronous with N line triggers, acceleration/deceleration table can be switched not only by the N line triggers (external triggers) but also by internal triggers. With this arrangement, acceleration and deceleration of the motor is permitted within the N line triggers (external triggers) while being synchronized with the N line triggers (external triggers), thereby reducing a burden of software processing and realizing motor control corresponding to interruption of one line during high speed reading.